

Appl. No. 10/767,248
Docket No. 2102397-992820
Response to Office Action of May 20, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A multi-operational amplifier system comprising:
a plurality of operational amplifiers; and
a controller to configure the plurality of operational amplifiers to form an adaptive input range of said system.
2. (original) The multi-operational amplifier system of claim 1 wherein an input of one of said plurality of operational amplifiers is coupled to an input of at least one other of said plurality of operational amplifiers.
3. (original) The multi-operational amplifier system of claim 1 wherein each of said plurality of operational amplifiers having a first input, the first input of one of said plurality of operational amplifiers being coupled to a first node, the first inputs of at least two others of said plurality of operational amplifiers being coupled to a second node, the second node being different than said first node.
4. (original) The multi-operational amplifier system of claim 1 wherein each operational amplifier includes a compensation network, and an output of one of said plurality of operational amplifiers is coupled to an input of a compensation network of at least one other of said plurality of operational amplifiers.
5. (original) A multi-operational amplifier system comprising:
a first operational amplifier having an input formed of a NZ NMOS transistor;
and

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a second operational amplifier including an input formed of an N-type NMOS transistor.

6. (original) A multi-operational amplifier system comprising:

a first operational amplifier having an input formed of an NZ NMOS transistor;

and

a second operational amplifier including an input formed of a PMOS transistor.

7. (original) The multi-operational amplifier system of claim 6 further comprising:

a third operational amplifier having an input formed of a N-type NMOS

transistor.

8. (original) A multi-operational amplifier system comprising a first operational

amplifier having an input formed of a NZ NMOS transistor and of an n-type NMOS transistor.

9. (original) A multi-operational amplifier system comprising:

a plurality of operational amplifiers, one of said operational amplifiers having a fixed bias, another of said operational amplifiers having an adaptively switchable bias; and
a controller to configure the plurality of operational amplifiers and to select the said bias.

10. (original) A multi-operational amplifier system comprising:

a plurality of operational amplifiers, one of said operational amplifiers having a switchable bias, another of said operational amplifiers having a switchable bias; and
a controller to configure the plurality of operational amplifiers and to select said bias.

11. (original) A multi-operational amplifier system comprising:

a plurality of operational amplifiers, one of said operational amplifiers having a switchable bias, another of said operational amplifiers having an adaptively switchable bias; and
a controller to configure the plurality of operational amplifiers to select said bias.

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12. (original) The multi-operational amplifier system of claim 11 wherein said another operational amplifier adaptively switches bias based on the switchable bias of said one operational amplifier.

13. (original) The multi-operational amplifier system of claim 11 wherein said another operational amplifier selectively switches a compensation network based on the compensation usage of said one operational amplifier.

14. (original) A multi-operational amplifier system comprising:
a first operational amplifier configured as an output transconductance amplifier;
a second operational amplifier configured as an output transconductance amplifier; and
a third operational amplifier configured as a folded cascode operational amplifier.

15. (original) The multi-operational amplifier system of claim 14 wherein said first operational amplifier includes a PMOS input differential pair.

16. (original) The multi-operational amplifier system of claim 14 wherein said second operational amplifier includes an NZ NMOS input differential pair.

17. (original) The multi-operational amplifier system of claim 14 wherein said third operational amplifier includes an N-type NMOS input differential pair.

18. (original) The multi-operational amplifier system of claim 14 wherein the first, second, and third operational amplifiers each comprise an output stage that includes a source follower.

19. (original) A multi-operational amplifier system comprising:
a first, second, and third operational amplifiers each configured as an output transconductance amplifier.

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20. (original) The multi-operational amplifier system of claim 19 wherein the first operational amplifier includes a PMOS input differential pair.

21. (original) The multi-operational amplifier system of claim 20 wherein said second operational amplifier includes a NZ NMOS input differential pair.

22. (original) The multi-operational amplifier system of claim 20 wherein each of the first, second and third operational amplifiers includes a configurable compensation network.

23. (original) The multi-operational amplifier system of claim 22 wherein the third operational amplifier includes a N-type NMOS input differential pair.

24. (original) A multi-operational amplifier system comprising:
a plurality of operational amplifiers; and
a configuration circuit to configure the plurality of operational amplifiers, said configurable circuit including one of non-volatile fuses, digital control signals, registers, or metallization interconnects.